

## Typical RF Circuit

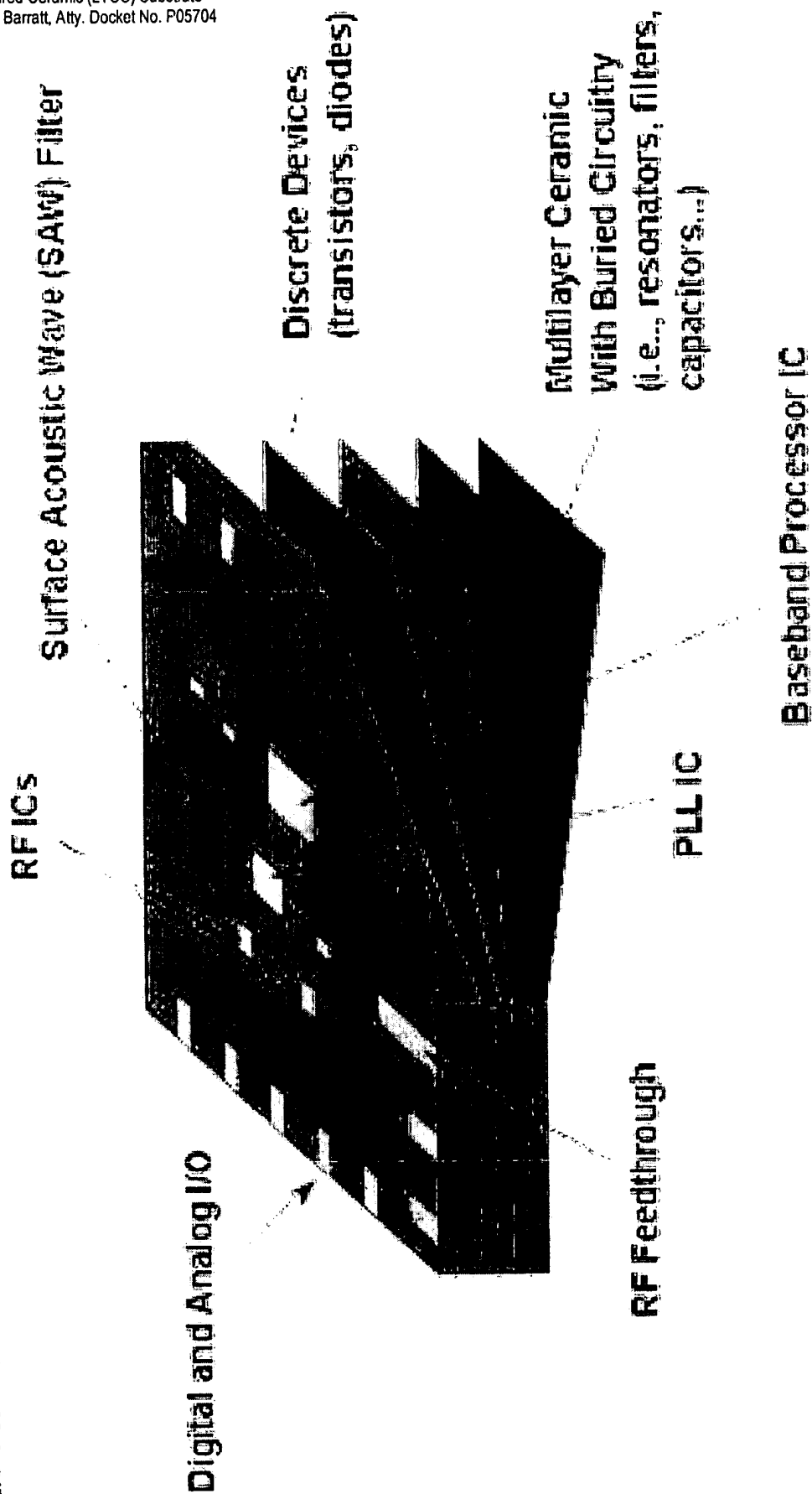


FIGURE 1  
(PRIOR ART)

Buried Self-Resonant Bypass Capacitors Within Multilayered Low  
Temperature Co-Fired Ceramic (LTCC) Substrate  
Inventor: Christopher Barratt, Atty. Docket No. P05704

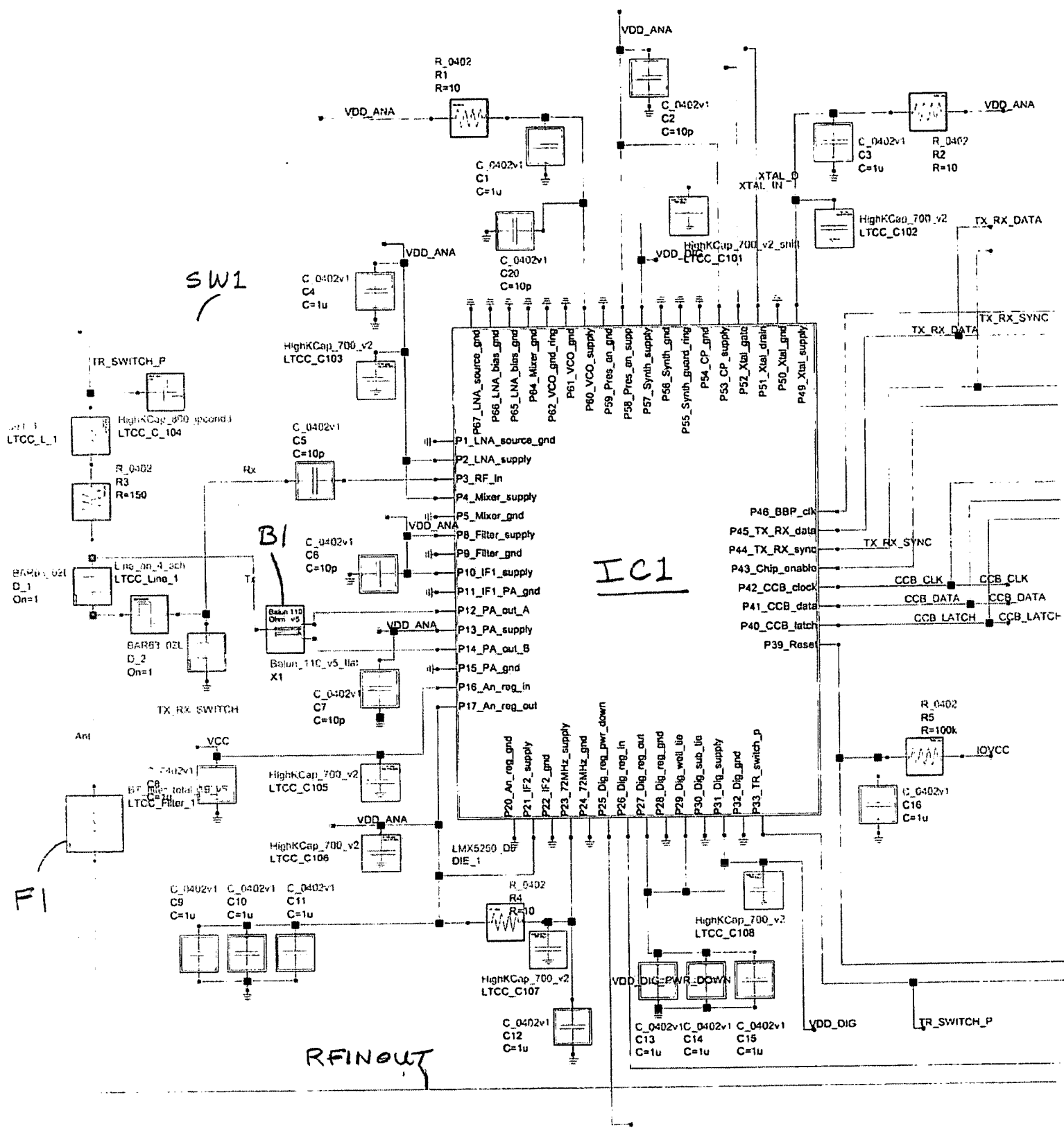


FIGURE 2A

2A	2B	2C
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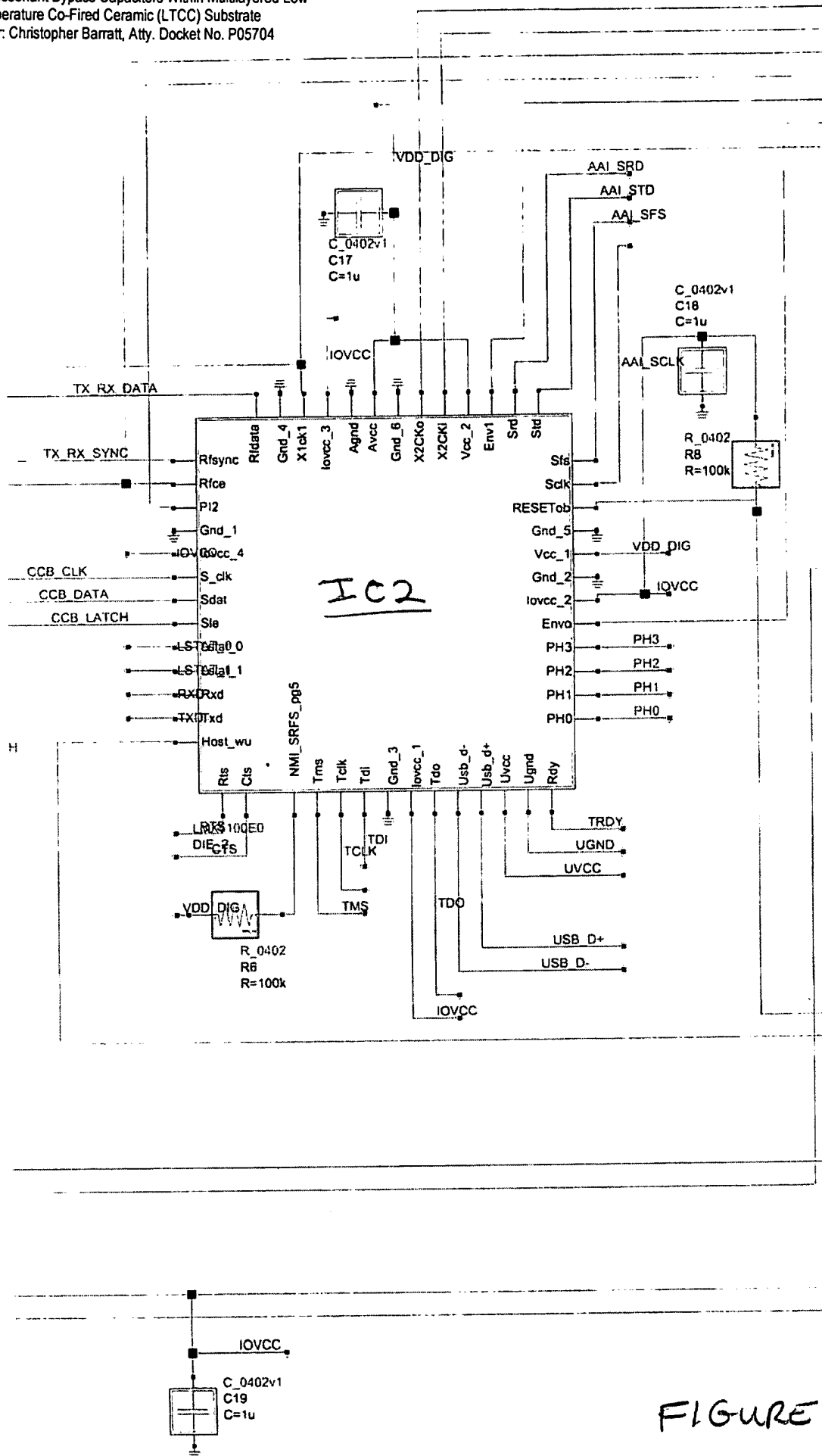


FIGURE 2B

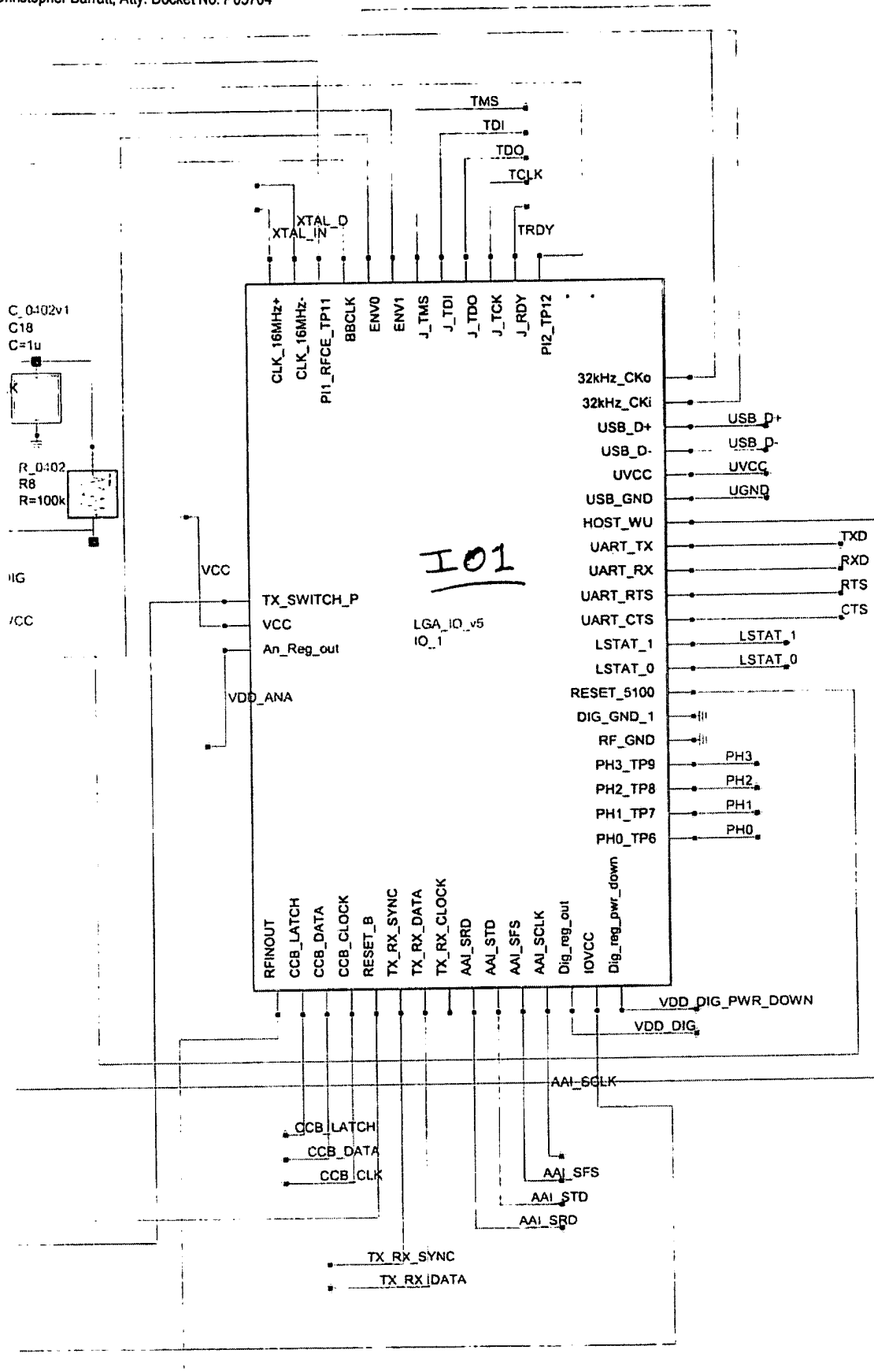
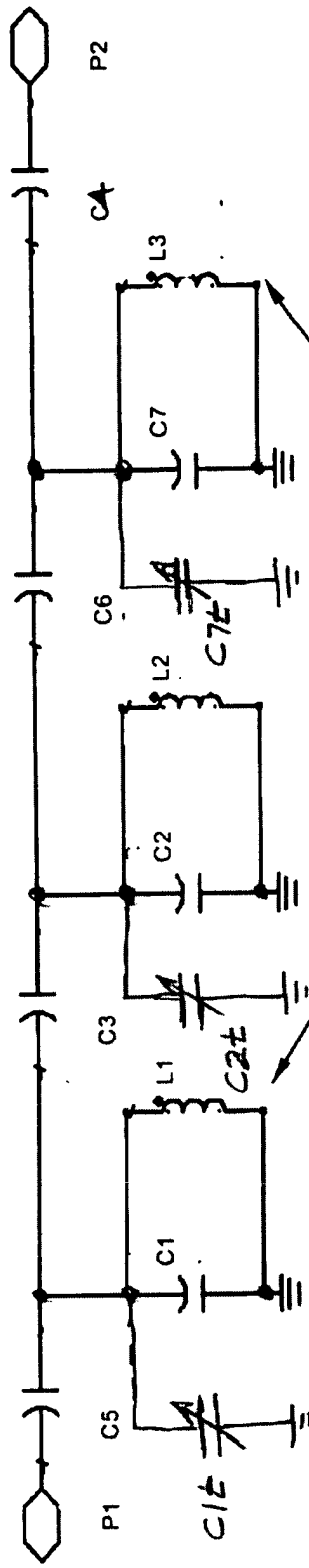


FIGURE 2C



F1 →

FIGURE 3

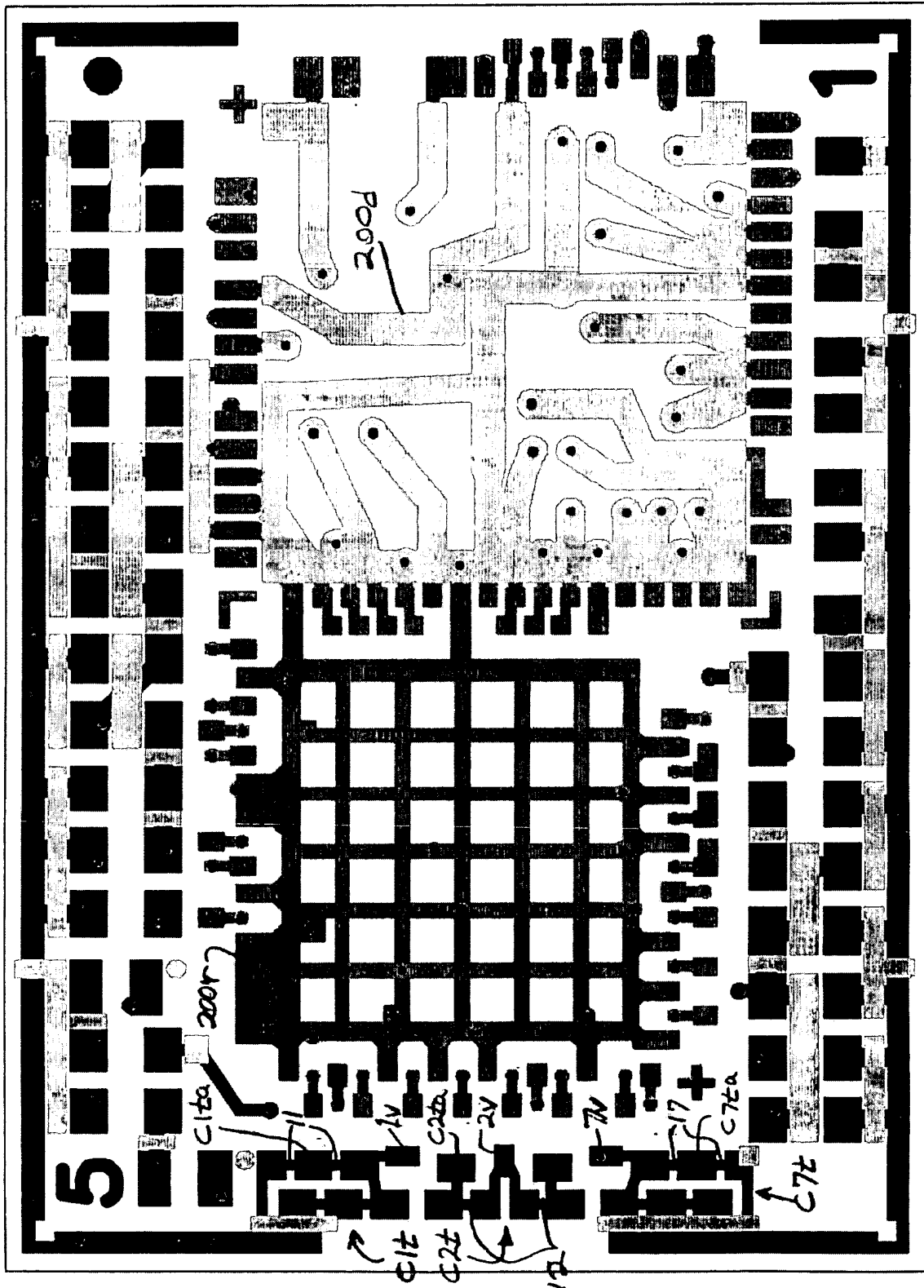
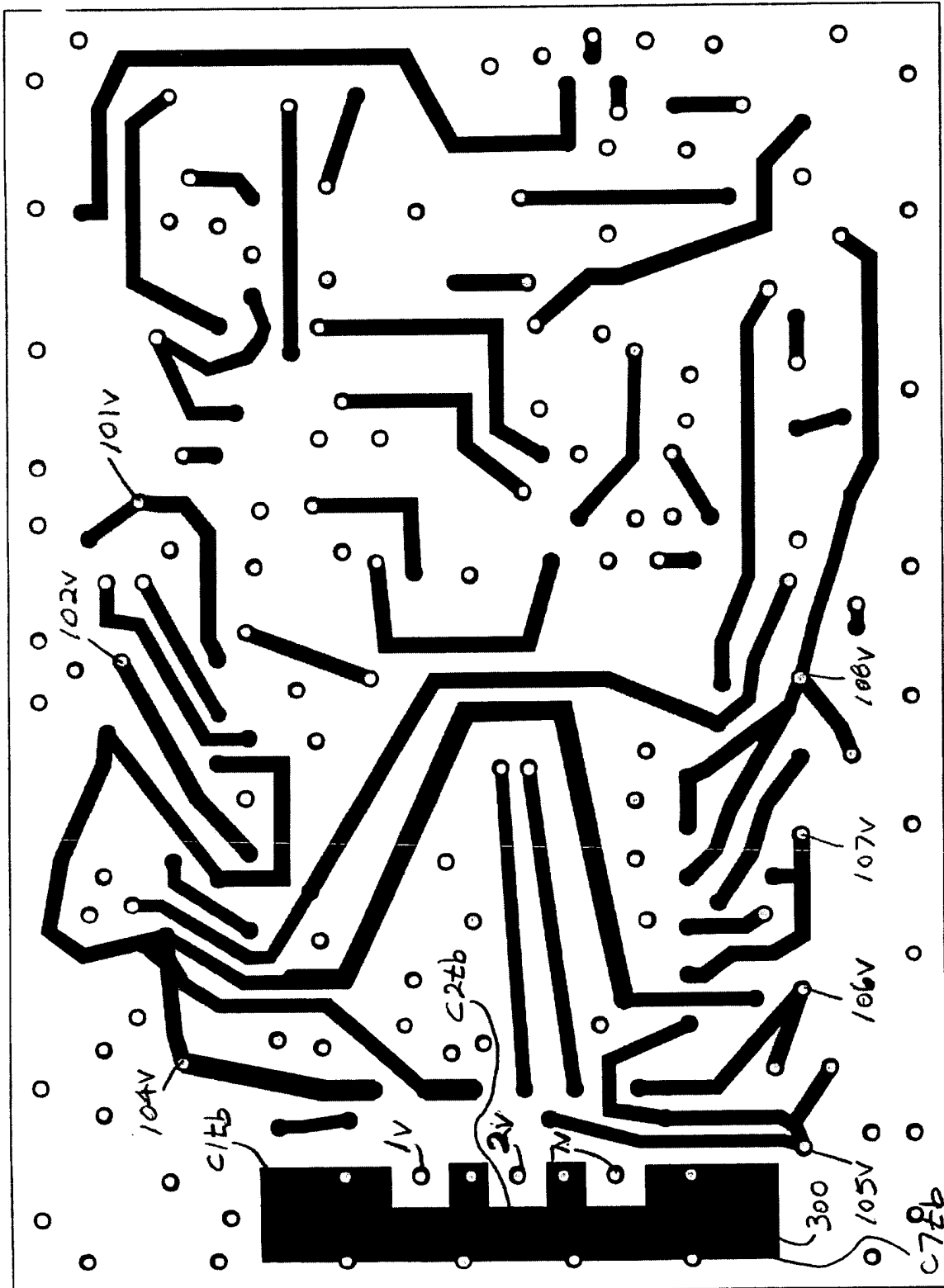
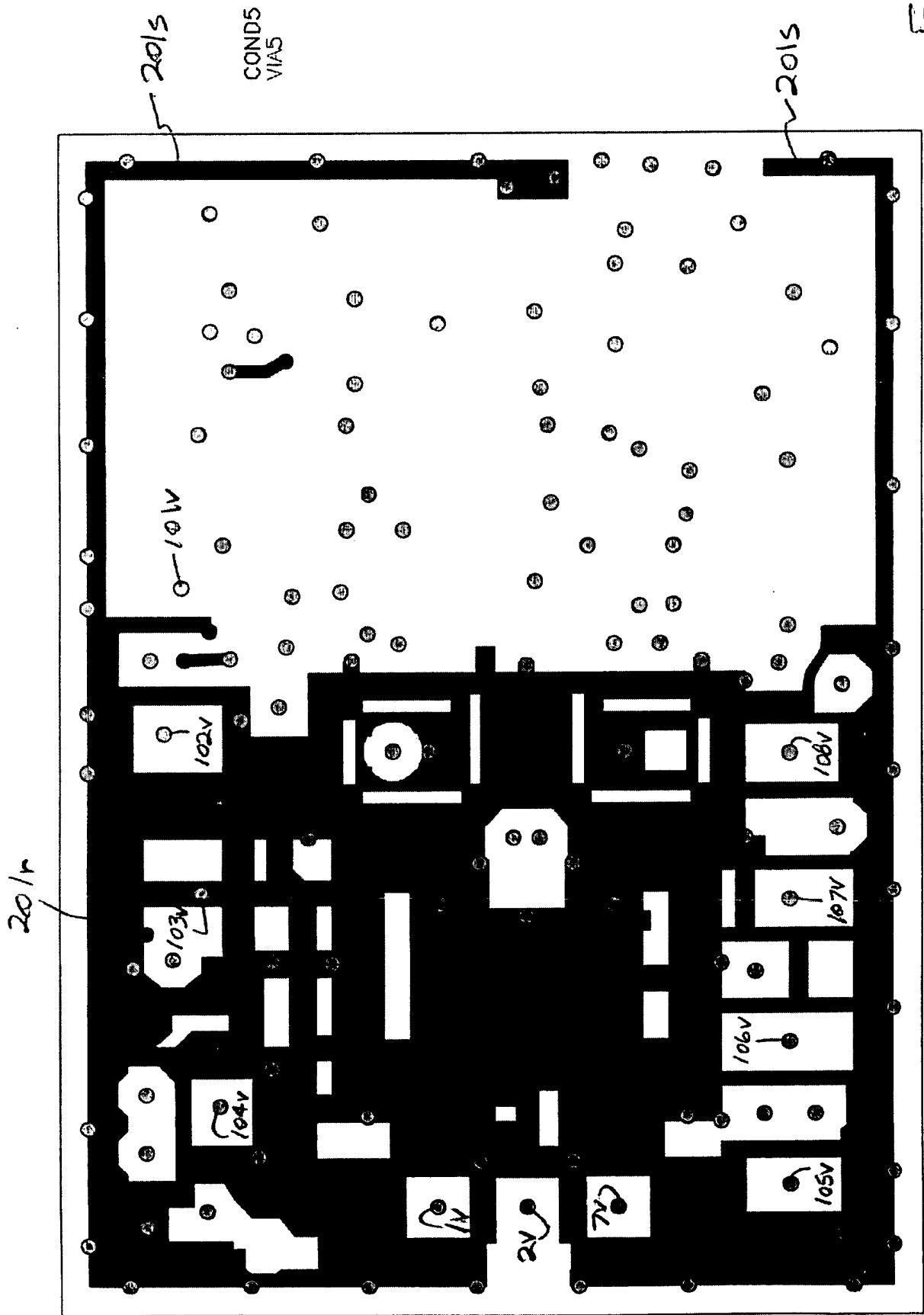


FIGURE  
 4B



COND6

FIGURE  
4C





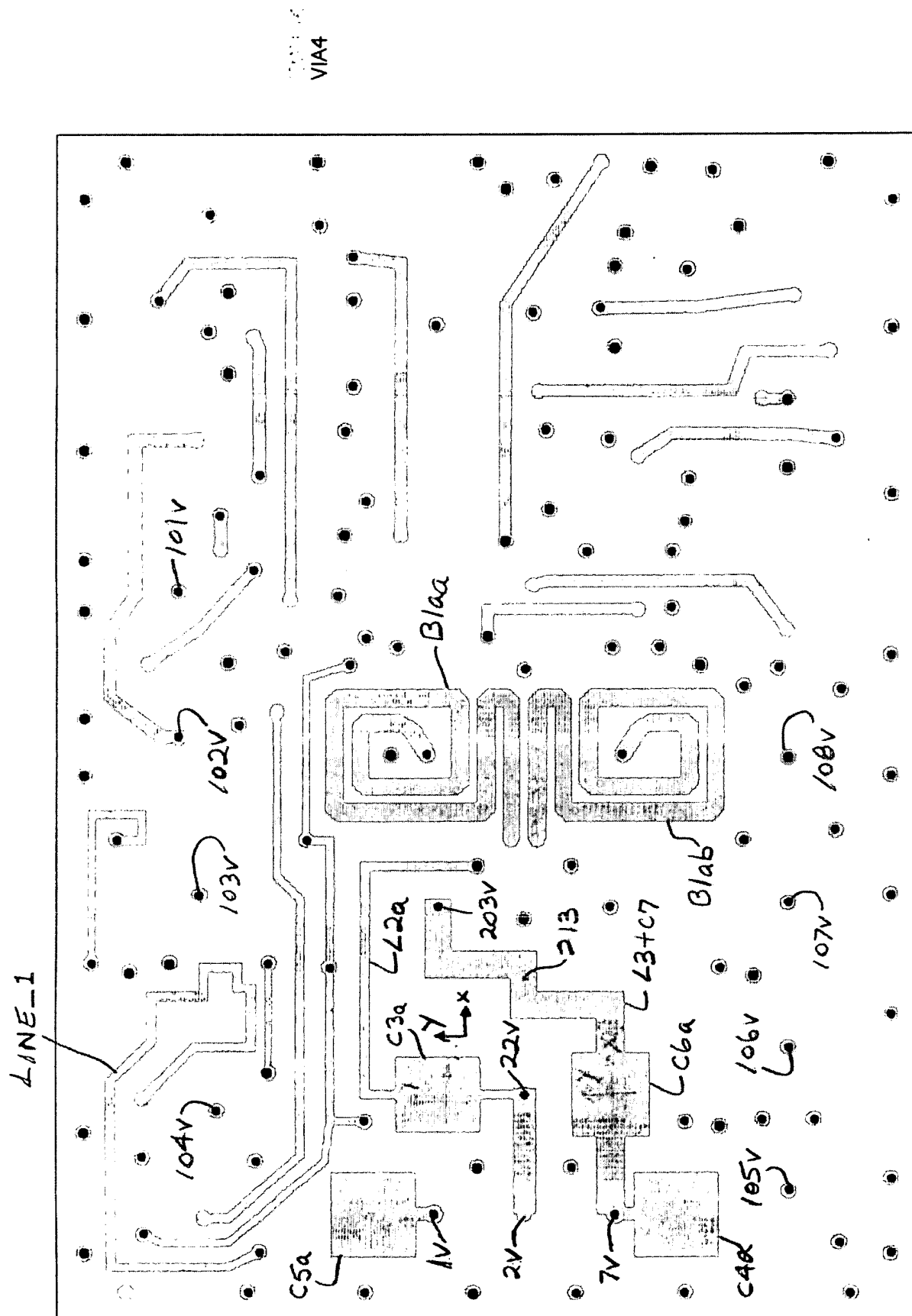
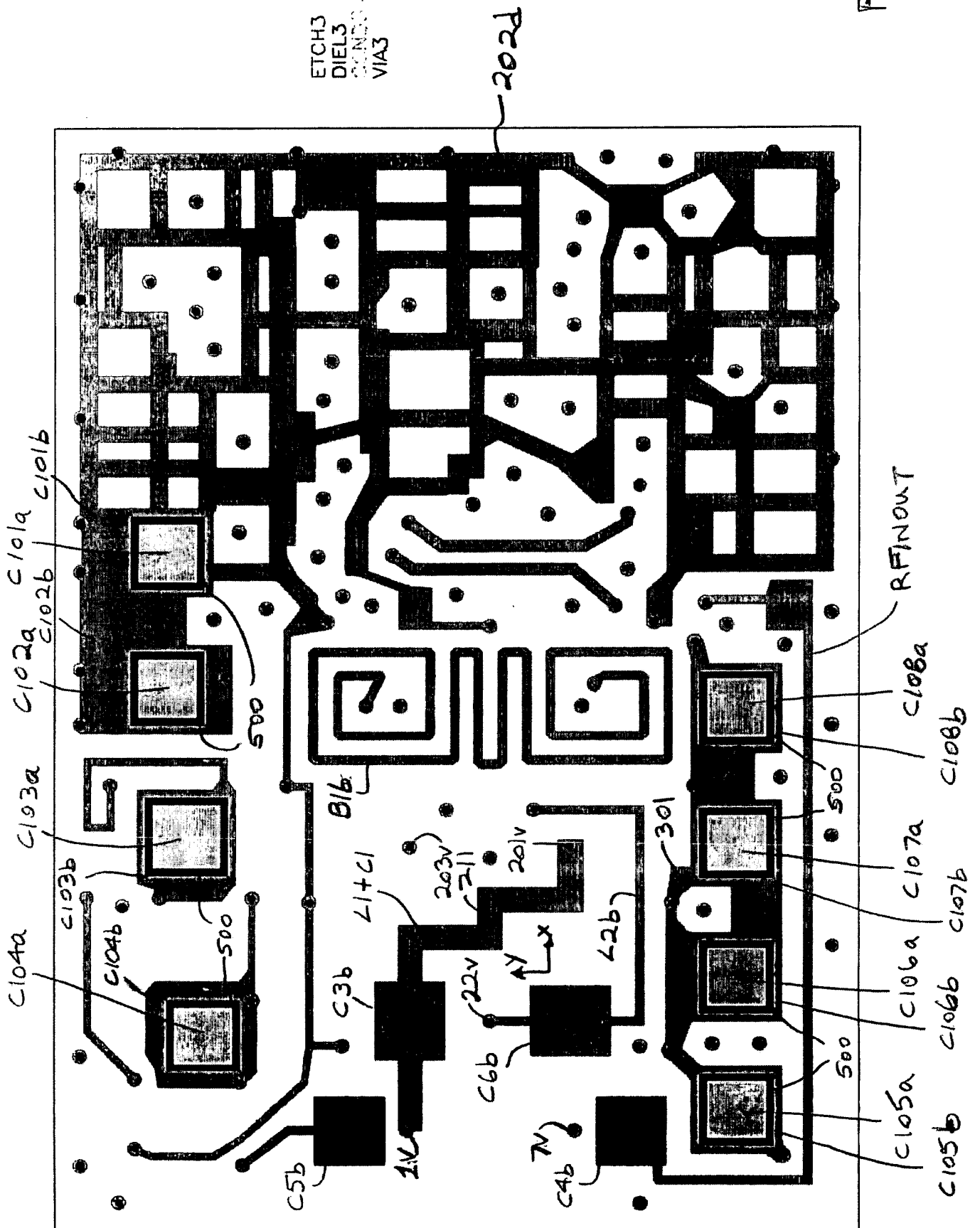


FIGURE 4E



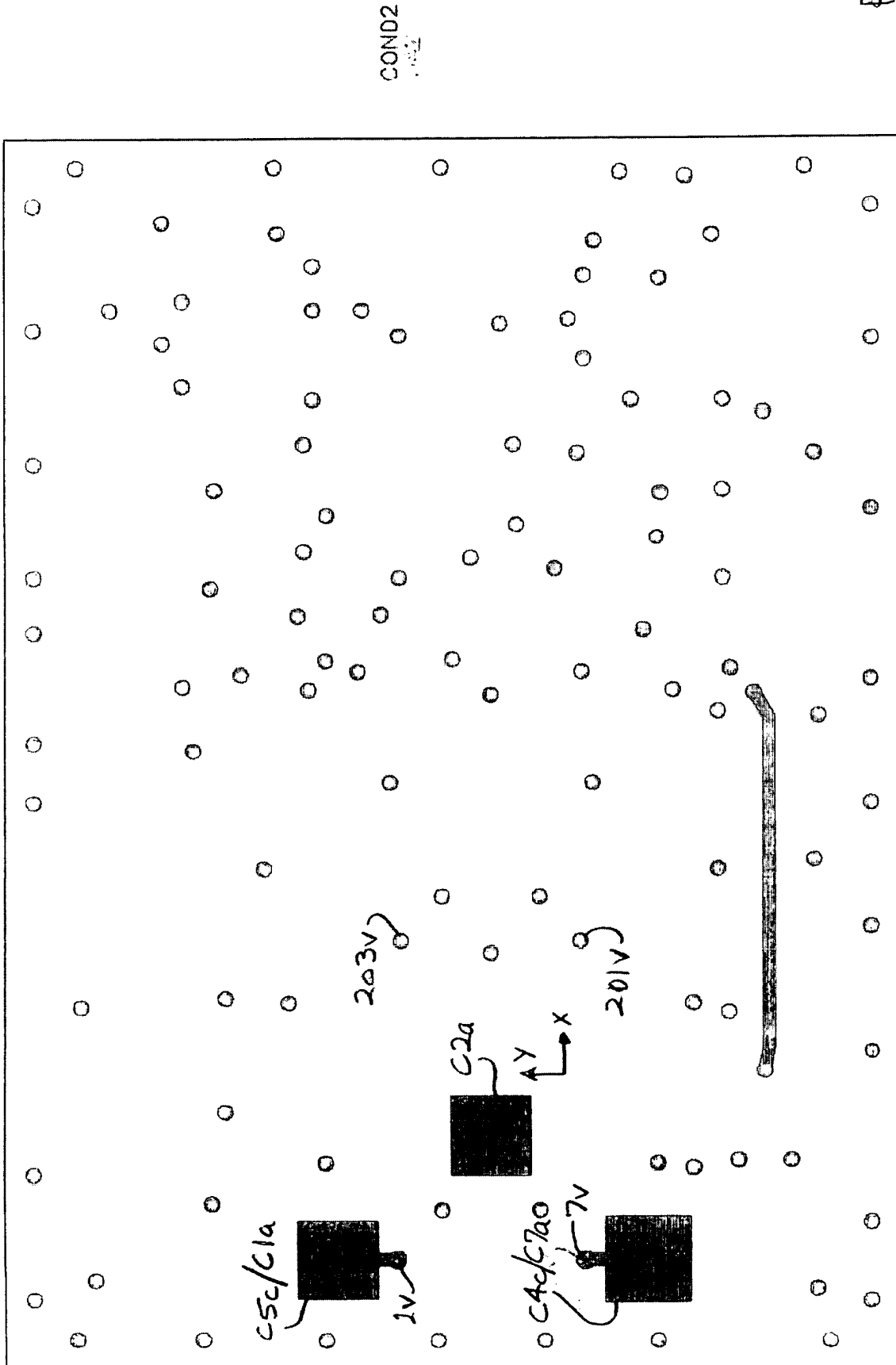
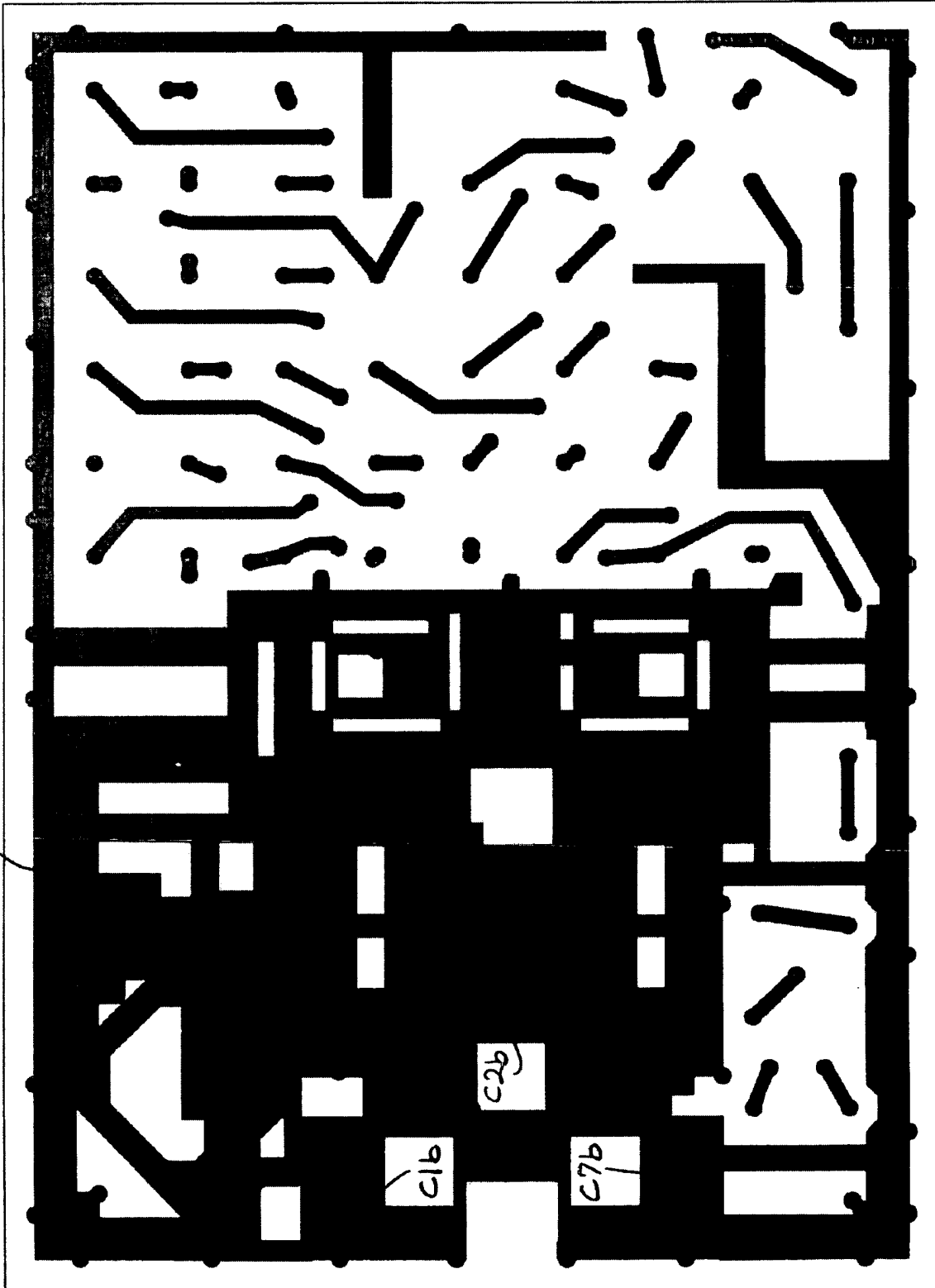


FIGURE  
4G

COND1  
VIA1

203r



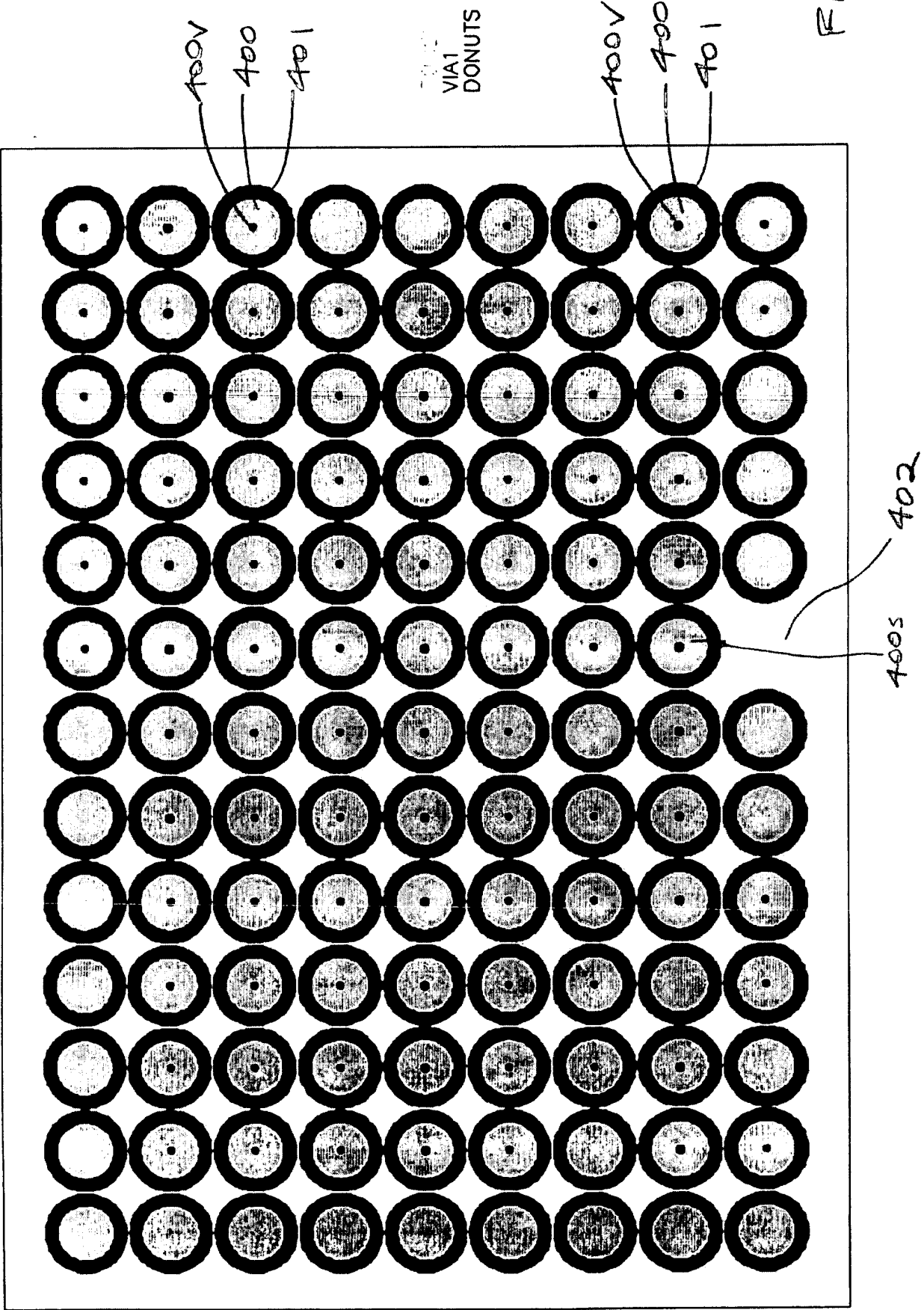


FIGURE  
4H

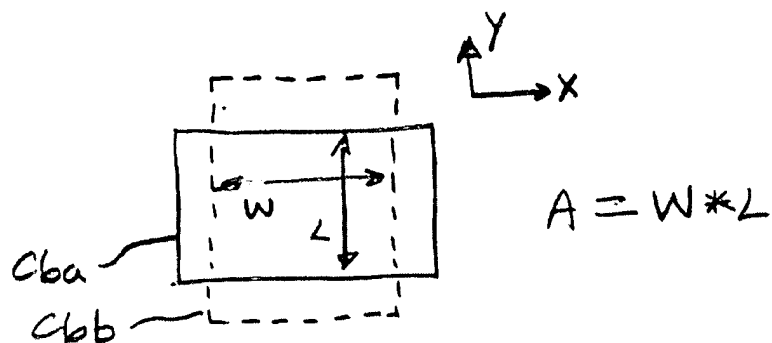


FIGURE 5

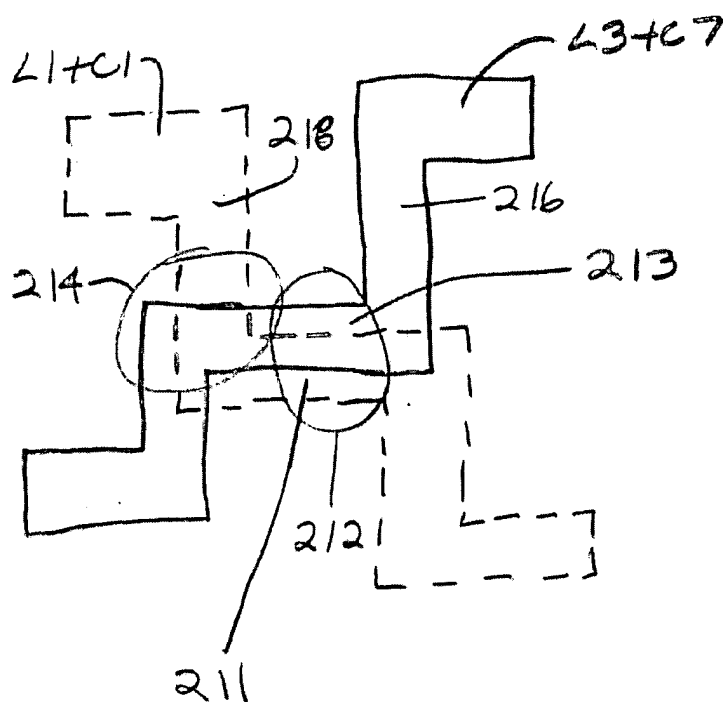


FIGURE 6

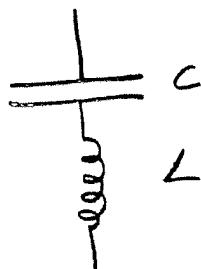


FIGURE 7